

Applicant: <u>Masanobu IWASAKI, et al.</u>		Docket No. <u>050090-0334</u>	Serial No. <u>09/934,474</u>
Title: <u>POLISHING SOLUTION SUPPLY SYSTEM, METHOD OF SUPPLYING POLISHING SOLUTION, APPARATUS FOR AND METHOD OF POLISHING SEMICONDUCTOR SUBSTRATE AND METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE</u>			
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<input type="checkbox"/> Other: _____	<input type="checkbox"/> CIP	<input type="checkbox"/> Div.	<input type="checkbox"/> PCT
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<input type="checkbox"/> Small Entity <input type="checkbox"/> Large Entity	<input type="checkbox"/> Div.	<input type="checkbox"/> PCT	<input type="checkbox"/> Rule 312 Amendment/Letter
<input type="checkbox"/> Declaration/Power of Attorney	<input type="checkbox"/> Div.	<input type="checkbox"/> PCT	<input type="checkbox"/> Req. for Acknowledgement of Cited Art
<input type="checkbox"/> Recordation of Assignment/Security Agreement	<input type="checkbox"/> Div.	<input type="checkbox"/> PCT	<input type="checkbox"/> Issue Fee
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<input type="checkbox"/> Certified Copy of Priority Doc.	<input type="checkbox"/> Div.	<input type="checkbox"/> PCT	<input type="checkbox"/> Status Inquiry
<input type="checkbox"/> Claim for Convention Priority	<input type="checkbox"/> Div.	<input type="checkbox"/> PCT	<input checked="" type="checkbox"/> Other <u>Response To Order Remanding To Examiner</u>
<input type="checkbox"/> Response/Amendment to Office Action of _____	<input type="checkbox"/> Div.	<input type="checkbox"/> PCT	
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CWS Descr.:

THE PATENT AND TRADEMARK OFFICE DATE STAMPED HEREON IS ACKNOWLEDGEMENT THAT THE ITEMS, CHECKED ABOVE, WERE RECEIVED BY THE PTO ON THE DATE STAMPED.

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Docket No.: 050090-0334

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of

Customer Number: 20277

Masanobu IWASAKI, et al.

Confirmation Number: 8431

Application No.: 09/934,474

Group Art Unit: 3723

Appeal No.: 2005-2579

Examiner: H. Shakeri

Filed: August 23, 2001

For: POLISHING SOLUTION SUPPLY SYSTEM, METHOD OF SUPPLYING POLISHING SOLUTION, APPARATUS FOR AND METHOD OF POLISHING SEMICONDUCTOR SUBSTRATE AND METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

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**RESPONSE TO ORDER REMANDING TO EXAMINER**

Mail Stop Appeal Brief - Patents  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Order Remanding to Examiner mailed November 16, 2005, Appellants submit herewith, a corrected claim Appendix. Appellants submit that the accompanying Appendix contains a correct copy of the claims involved in the present Appeal. The discrepancies noted by the Program and Resource Administrator with respect to claims 1, 6, 8 and 9 have been corrected by Appellants. Claims 1, 6, 8 and 9 now match the claims as submitted in the Amendment of March 4, 2002. Moreover, with respect to claim 15, Appellants acknowledge the presence of unequal brackets in the marked-up version of claim 15 submitted in the Amendment of March 4, 2002. However, the clean version of claim 15, as presented in the

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Amendment of March 4, 2002, accurately reflects the language Appellants intended to submit with the Amendment of March 4, 2002.

Further, Appellants telephoned Examiner Shakeri on February 7, 2006 to discuss claim 15. Examiner Shakeri indicated during the telephone interview that the language of claim 15, as recited in the clean version, was used as the basis for the final rejection dated April 18, 2002. The language of claim 15, as now presented in the attached Appendix, matches that of the clean version of claim 15.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due under 37 C.F.R. 1.17 and 41.20, and in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Date: February 7, 2006

Please recognize our Customer No. 20277  
as our correspondence address.

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